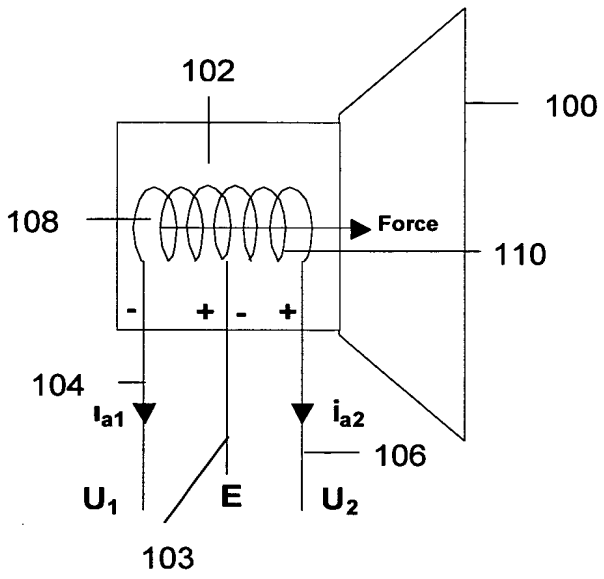

Figure 1

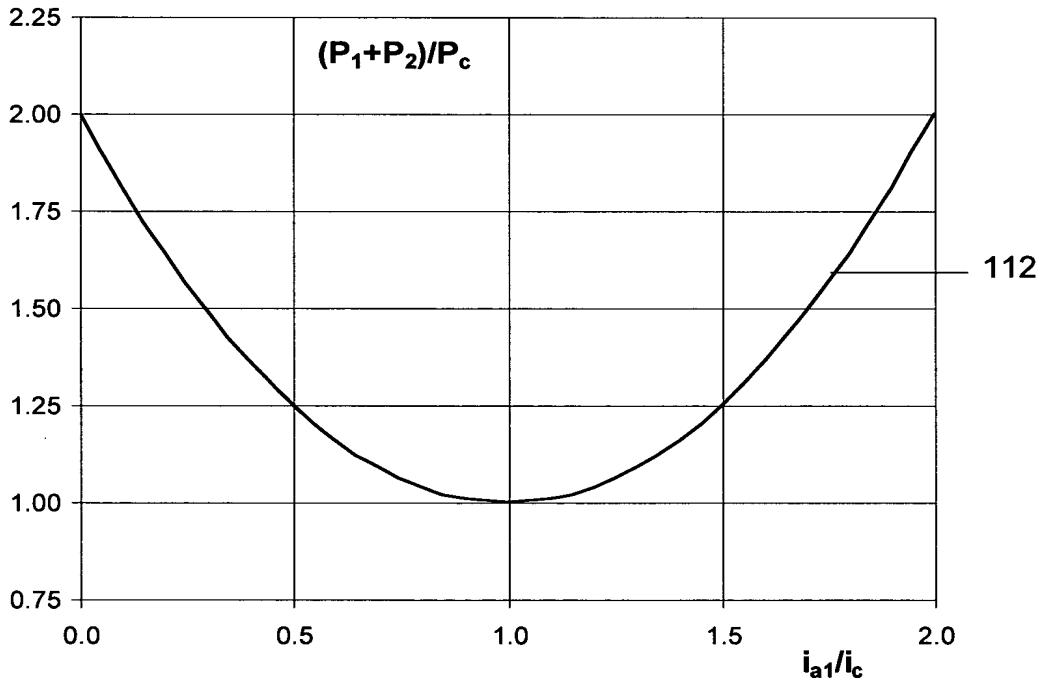

Figure 2

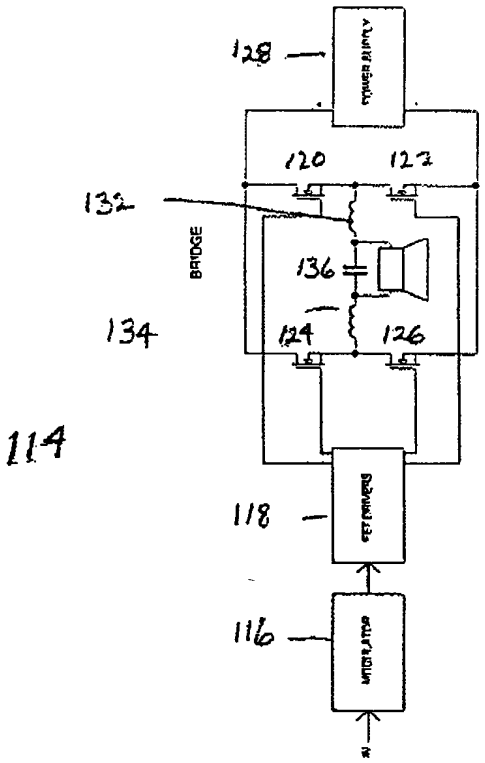

Figure 3A

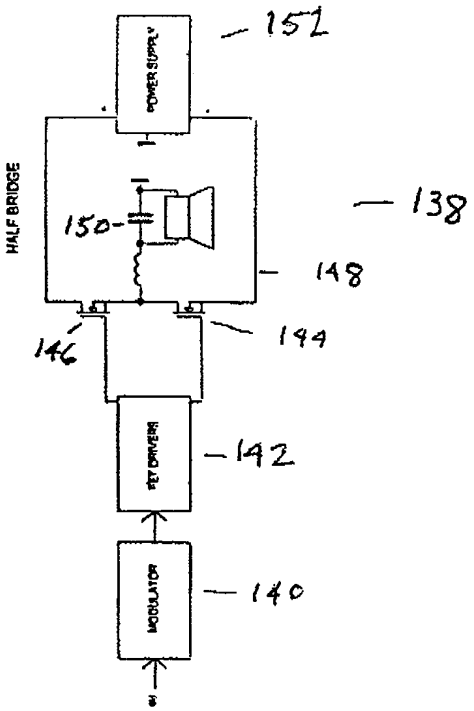

Figure 3B

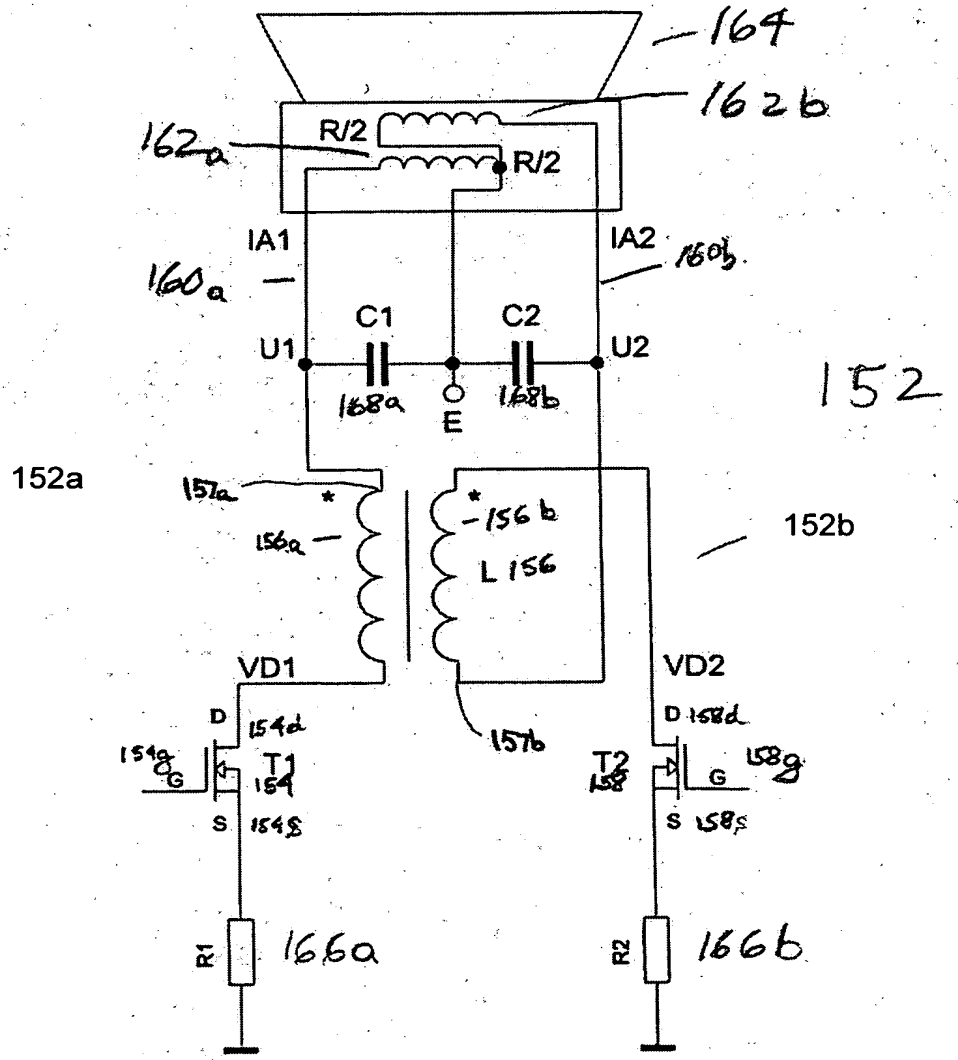
164
162b
162a
R/2
R/2
IA1
IA2
160a
160b
C1
C2
U1
U2
168a
168b
E
152
152a
152b
157a
156a
156b
L 156
VD1
VD2
D
154d
154g
G
T1
154
S
154s
157b
D
158d
T2
158
158g
G
S
158s
R1
166a
R2
166b

Figure 4

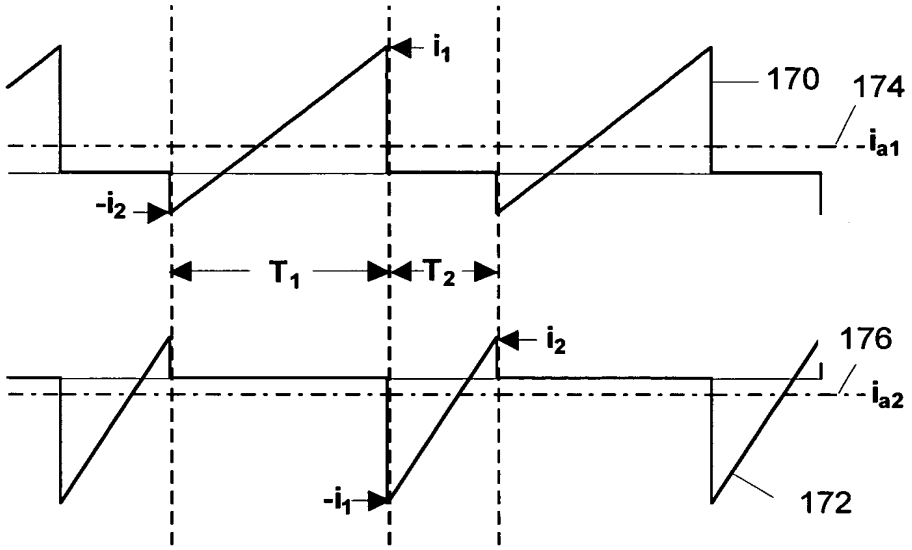

Figure 5

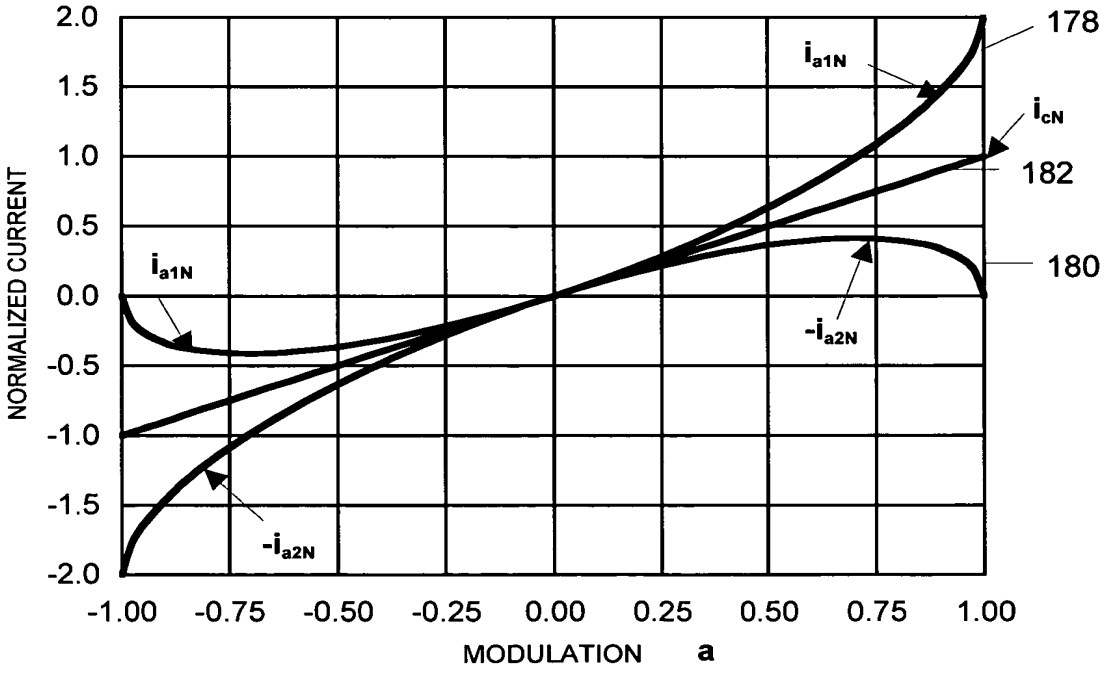

Figure 6

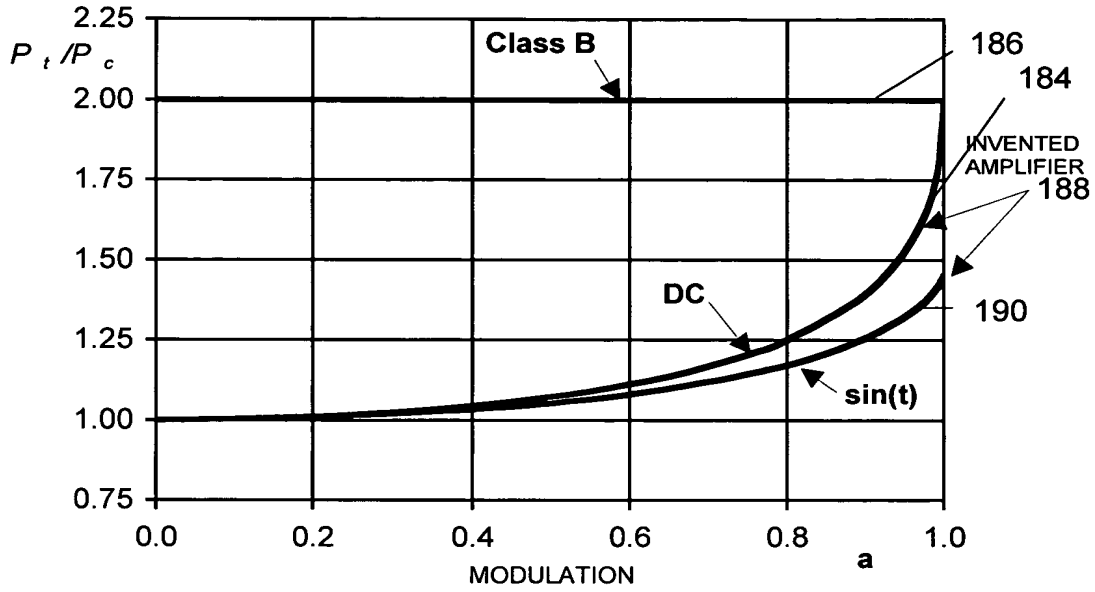

Figure 7

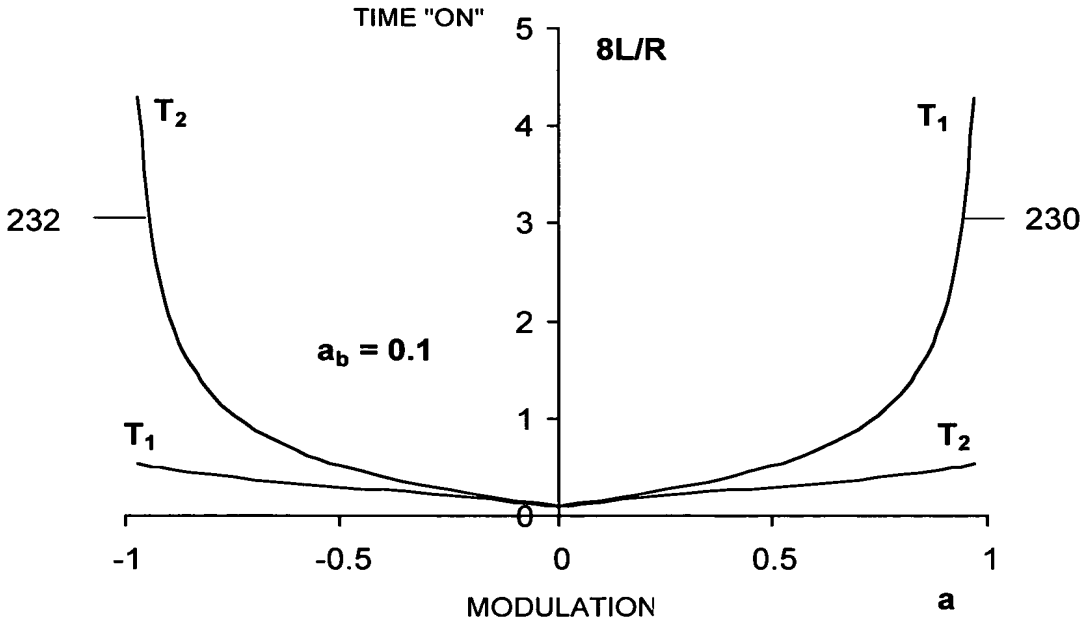

Figure 9

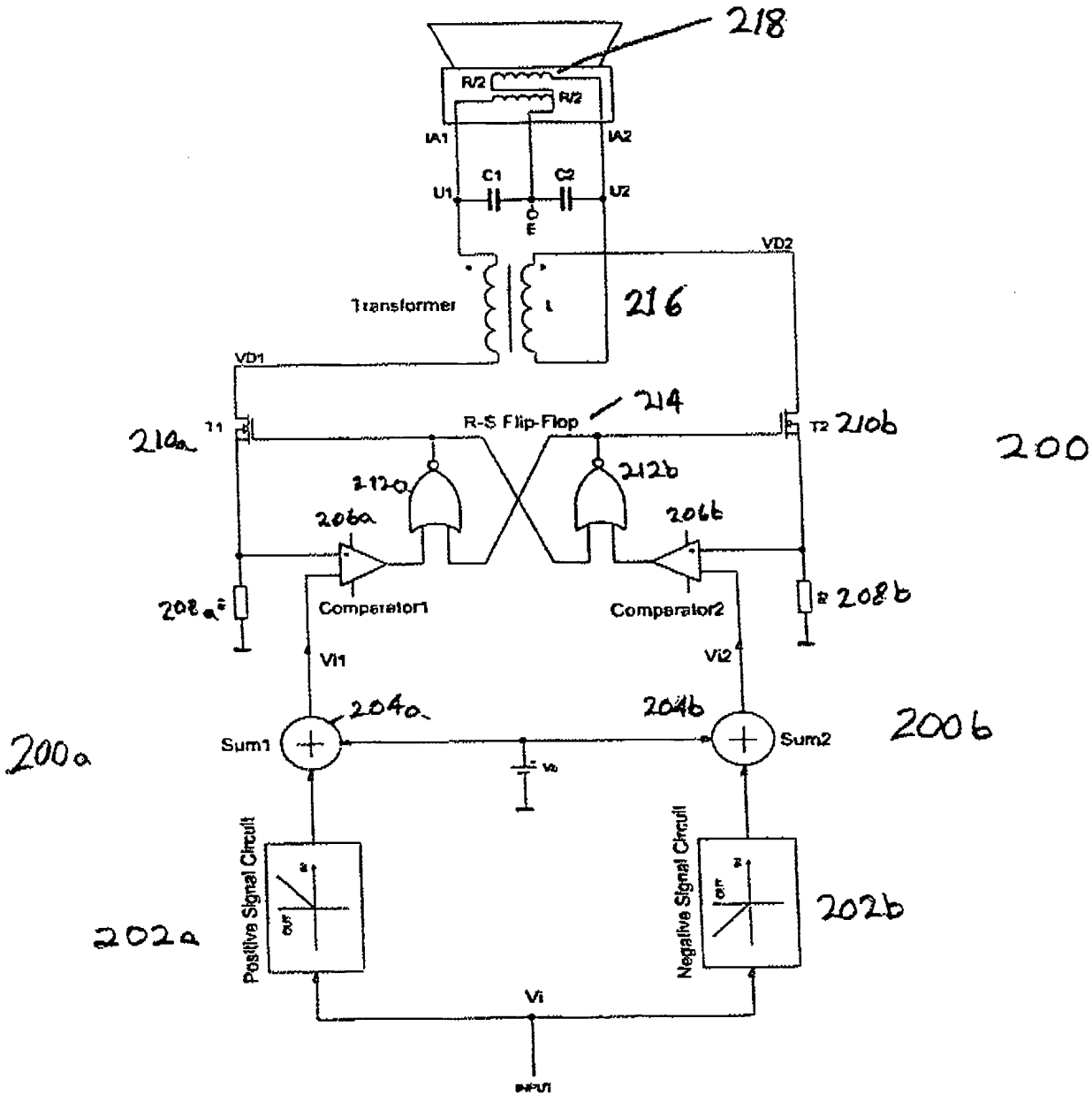
218
R/2
R/2
IA1
IA2
C1
C2
U1
U2
E
VD2
Transformer
L
216
VD1
214
R-S Flip-Flop
T1
T2
210a
210b
200
212a
212b
206a
206b
208a
208b
Comparator1
Comparator2
Vi1
Vi2
204a
204b
200a
200b
Sum1
Sum2
Vo
Positive Signal Circuit
Negative Signal Circuit
202a
202b
Vi
INPUT

Figure 8

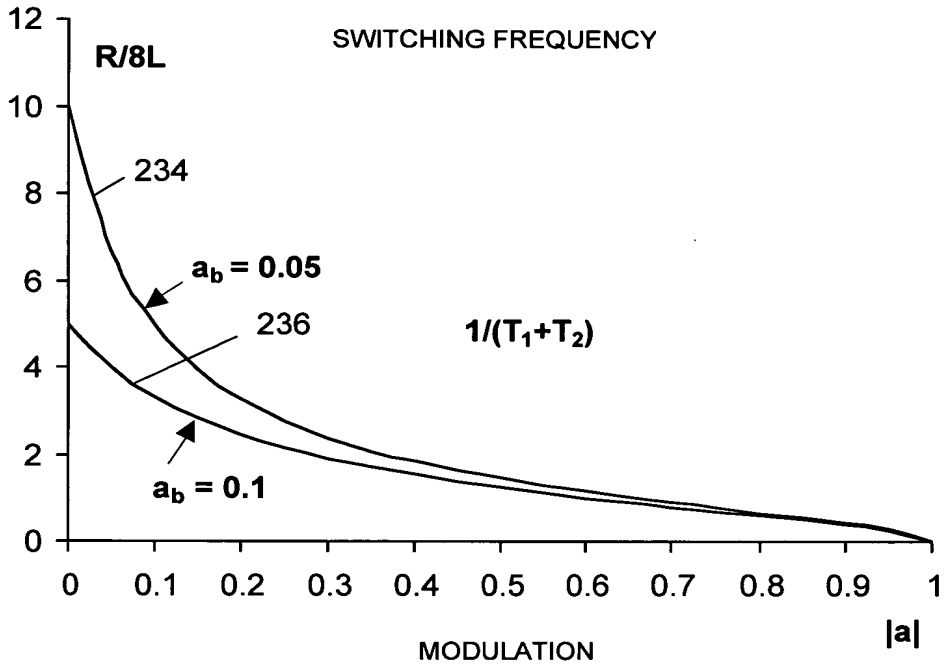

Figure 10

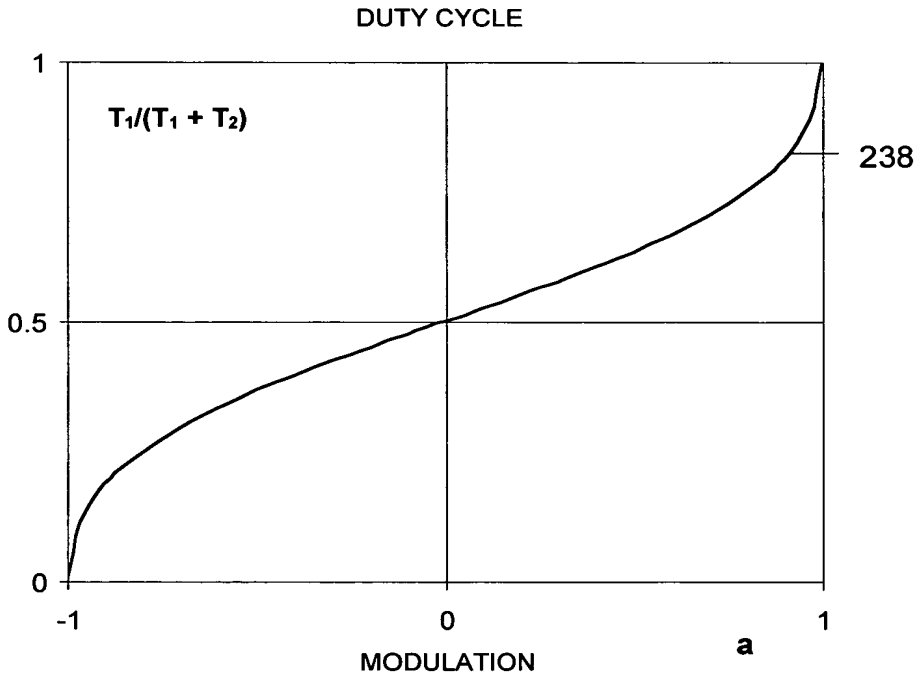

Figure 11

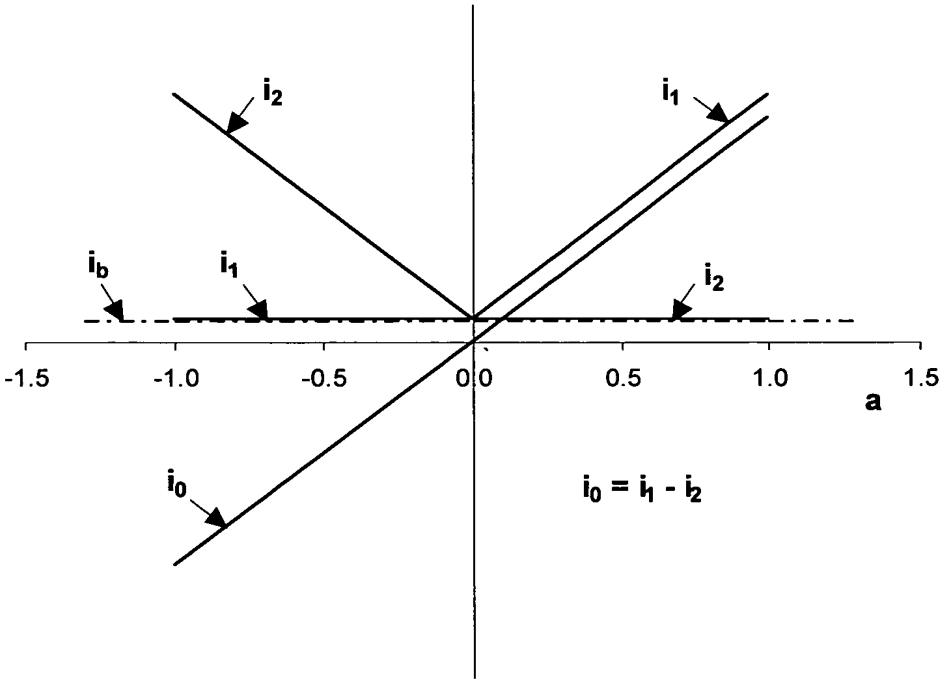

Figure 12

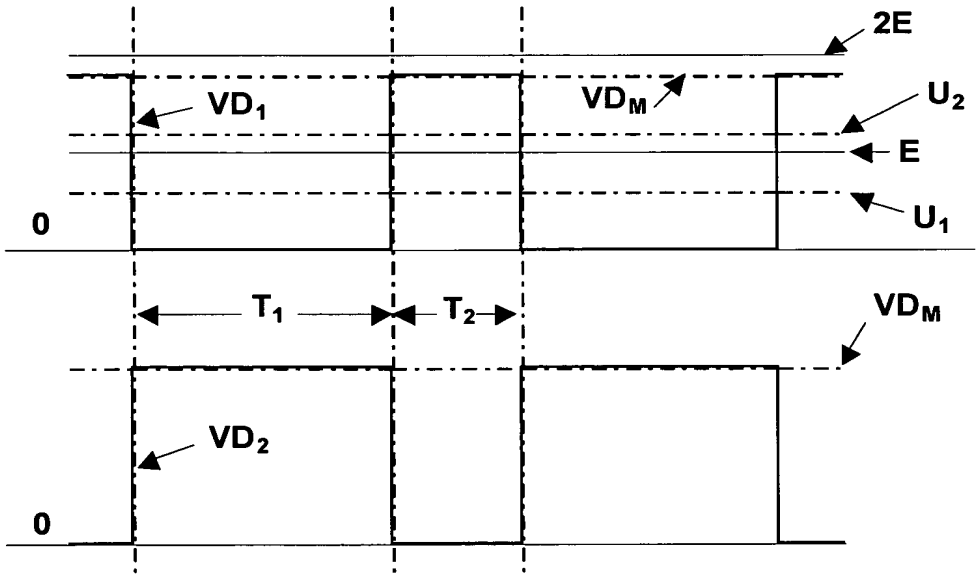

Figure 13

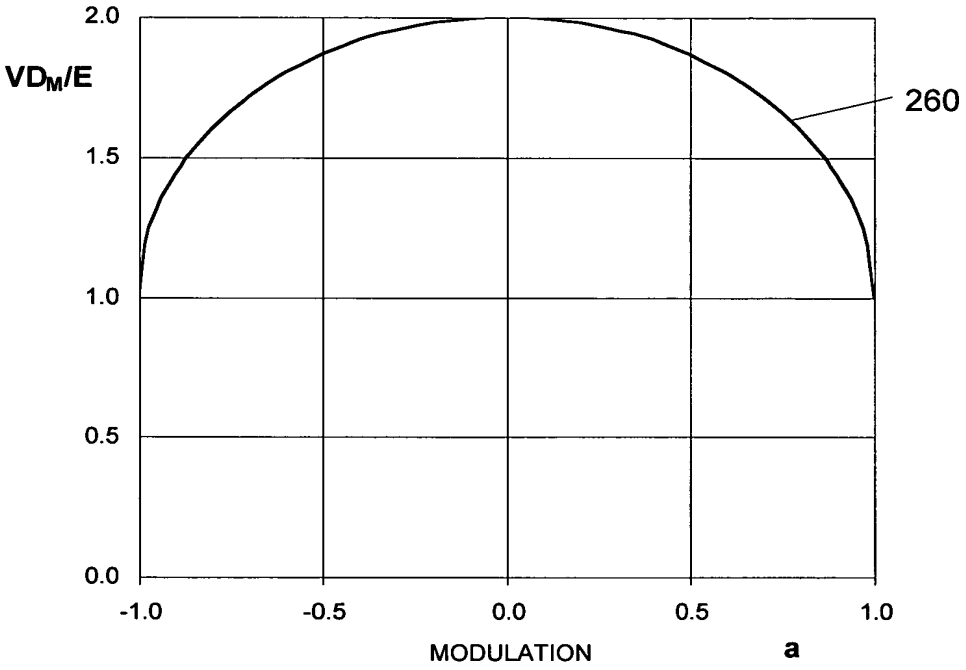

Figure 14

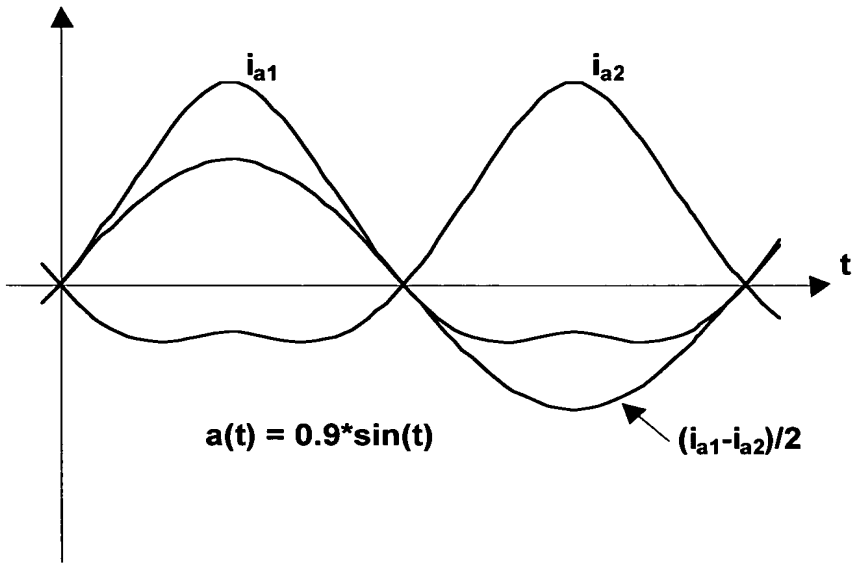

Figure 15

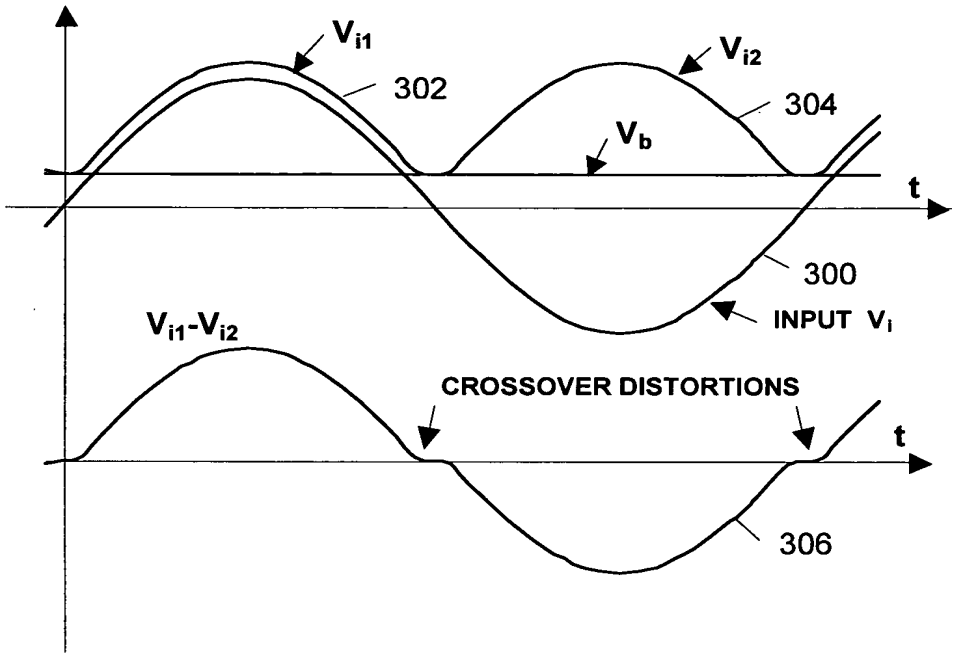

Figure 16

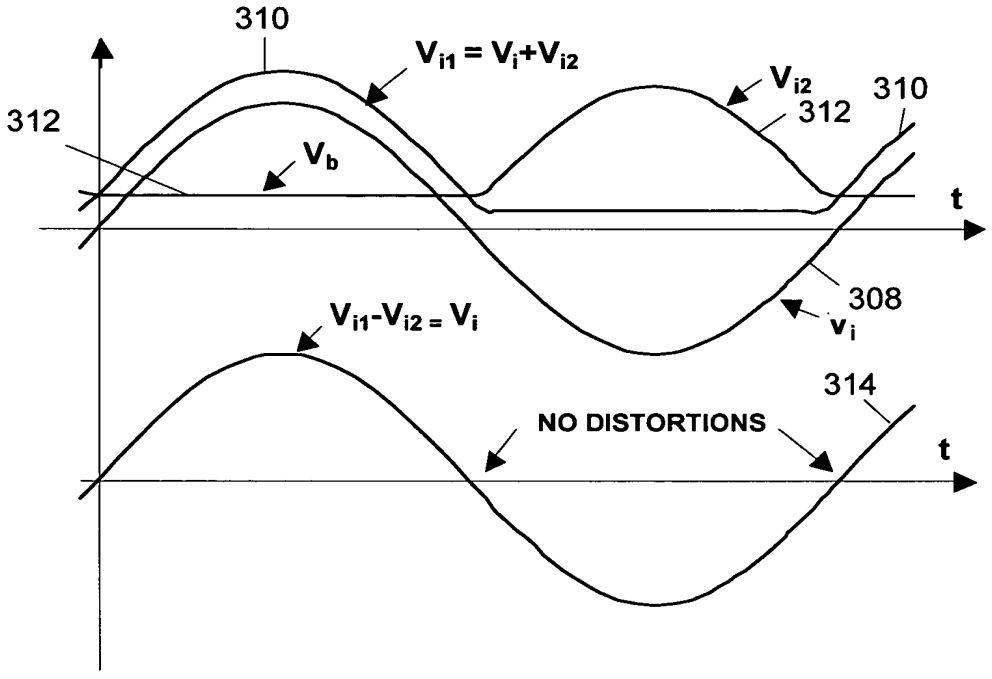

Figure 17

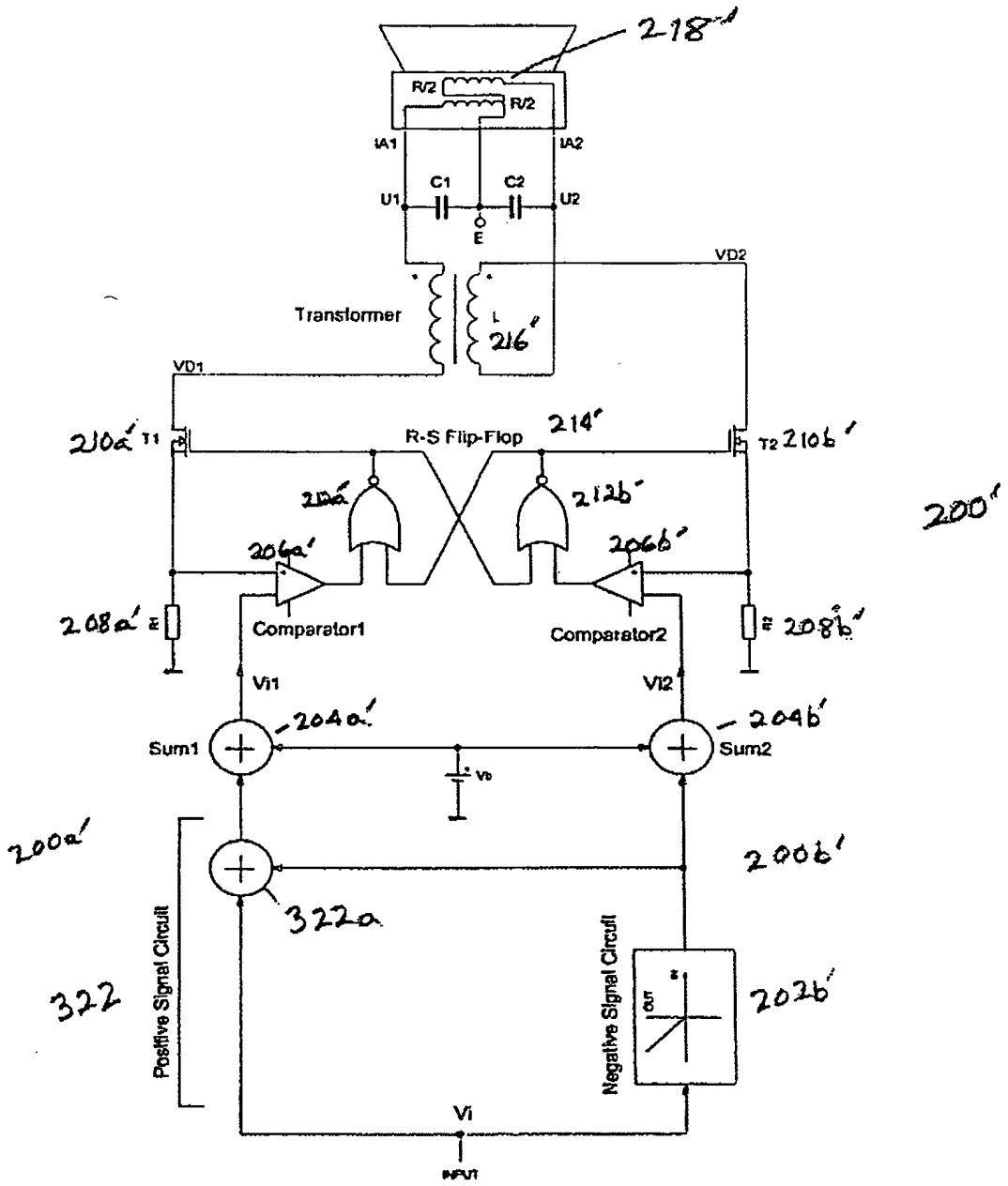
218'
R/2
R/2
IA1
IA2
C1
C2
U1
U2
E
VD2
Transformer
L
216'
VD1
210a'
T1
R-S Flip-Flop
214'
T2
210b'
200'
212a'
212b'
206a'
206b'
208a'
R1
Comparator1
Comparator2
R2
208b'
Vi1
Vi2
204a'
204b'
Sum1
Sum2
Vb
200a'
200b'
322a
322
Positive Signal Circuit
Negative Signal Circuit
202b'
Vi
INPUT

Figure 18

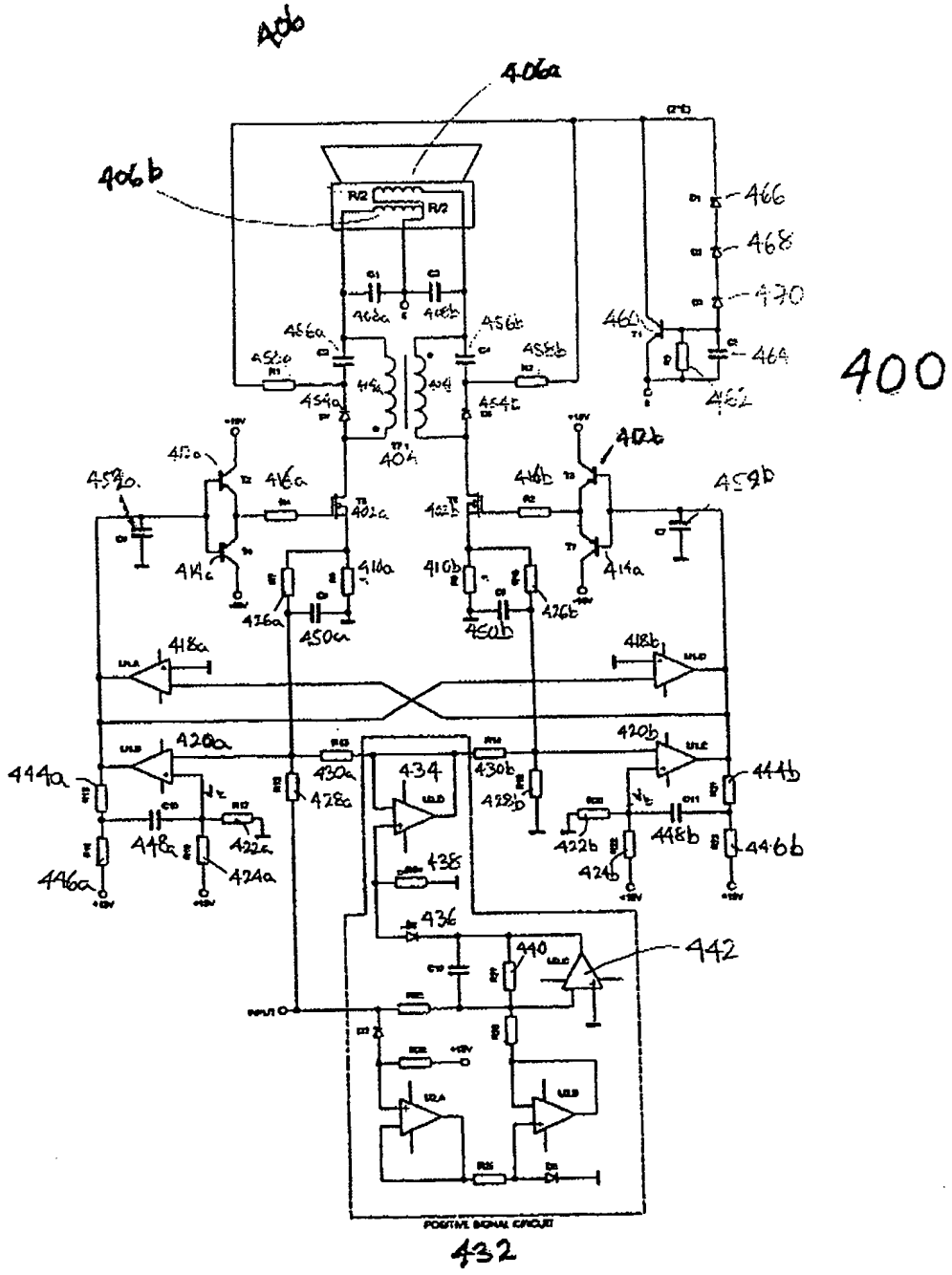
406b
406a
R/2
R/2
466
468
470
464
462
400
456a
456b
454a
454b
404
458a
458b
416a
416b
414a
414b
410a
410b
426a
426b
450a
450b
418a
418b
420a
420b
434
430a
430b
428a
428b
444a
444b
448a
448b
422a
422b
424a
424b
446a
446b
435
436
440
442
POSITIVE SIGNAL CIRCUIT
432

Figure 19

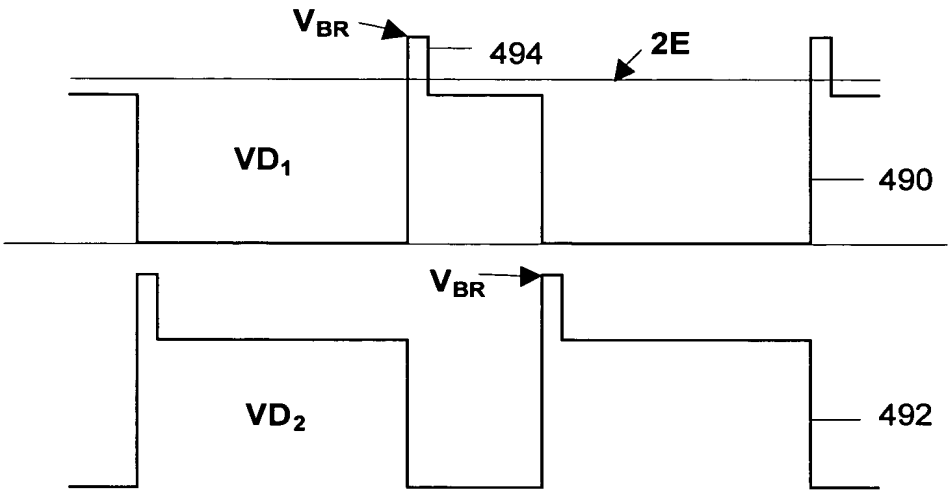

Figure 20

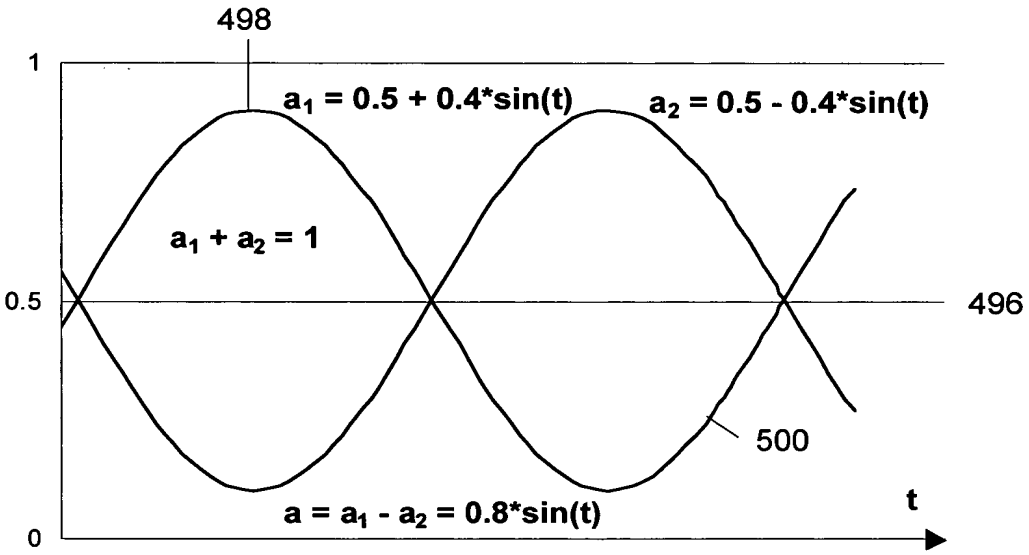

Figure 21

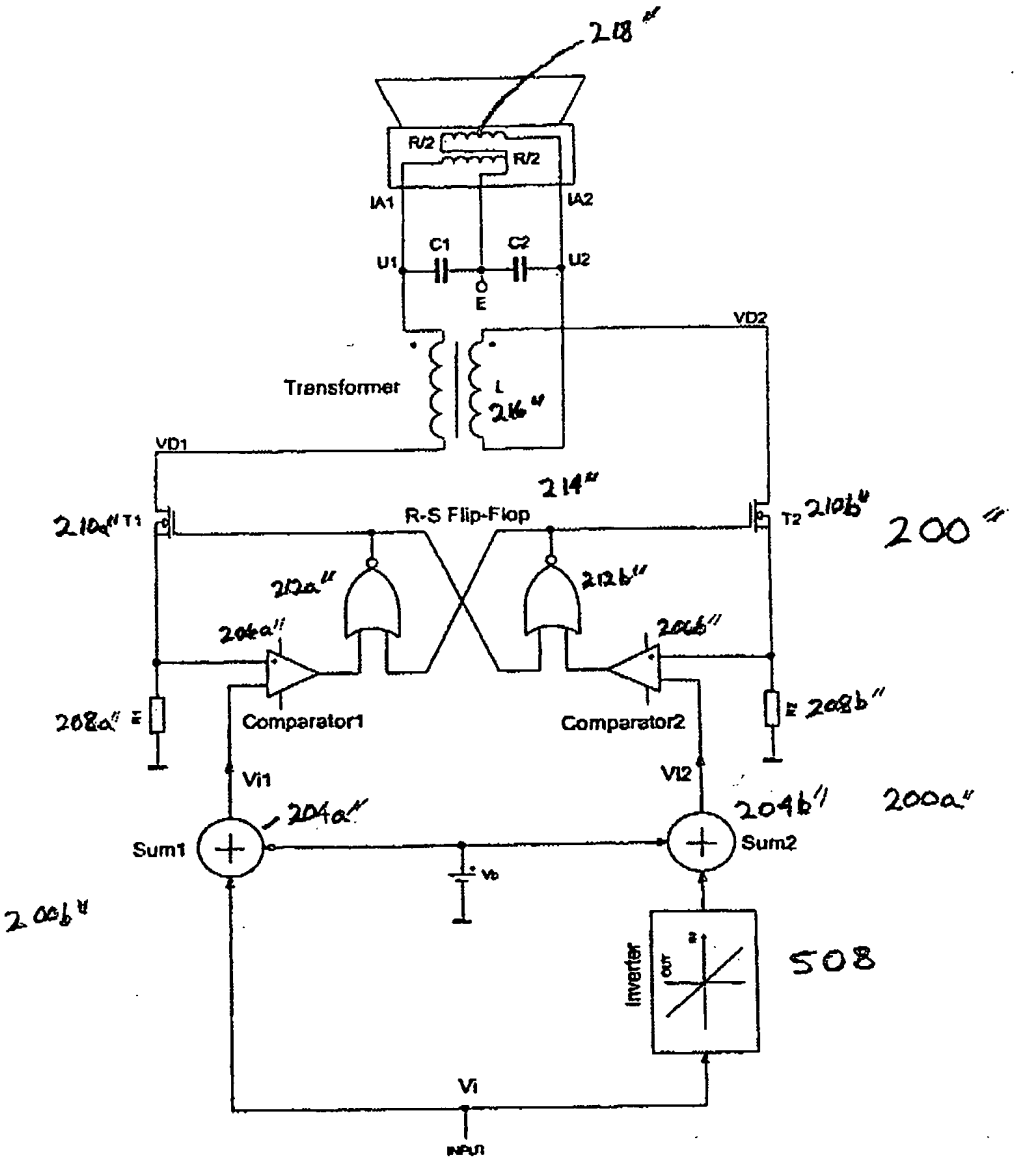
R/2
R/2
IA1
IA2
U1
C1
C2
U2
E
VD2
Transformer
L
VD1
R-S Flip-Flop
T1
T2
Comparator1
Comparator2
Vi1
Vi2
Sum1
Sum2
Vb
Inverter
Vi
INPUT

Figure 22

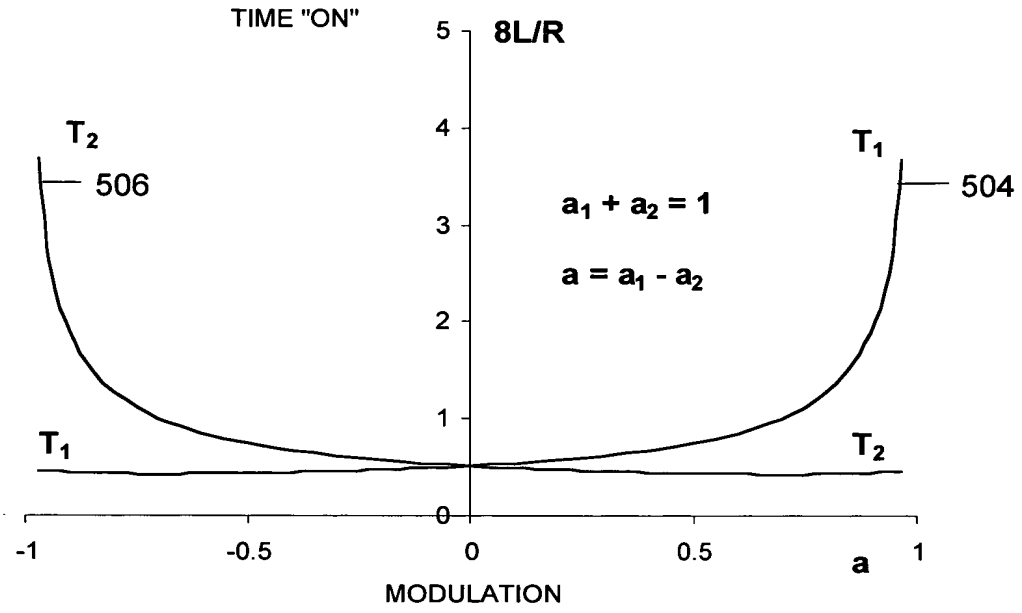

Figure 23

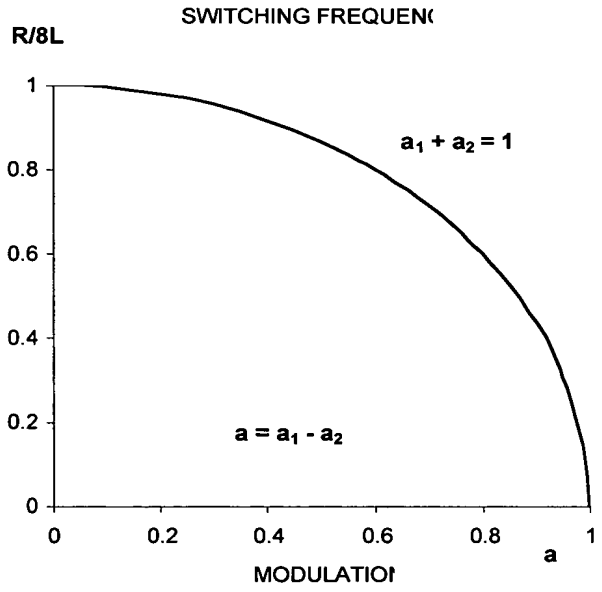

Figure 24

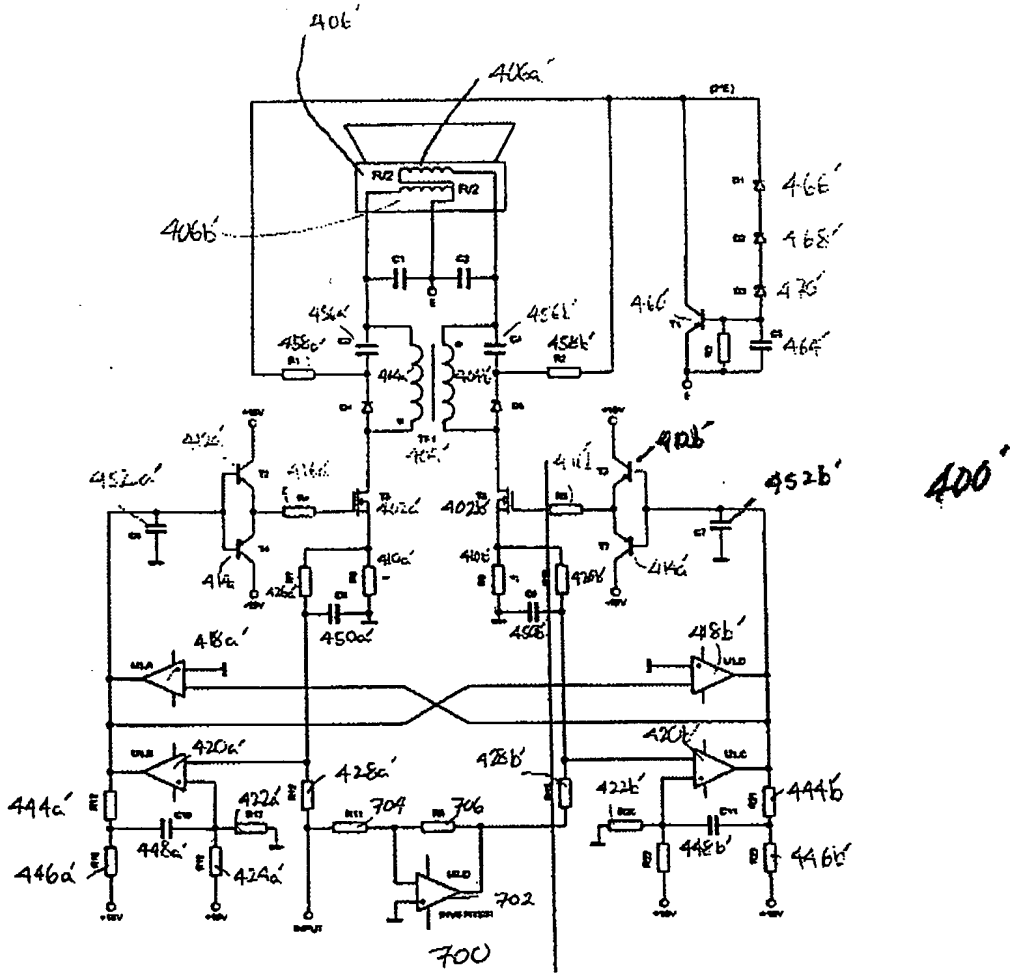
406'
406a'
406b'
466'
468'
470'
464'
452a'
452b'
400'
448a'
448b'
444a'
446a'
444b'
446b'
420a'
420b'
428a'
428b'
422a'
422b'
424a'
424b'
450a'
450b'
709
706
702
700

Figure 25